

FIG.2

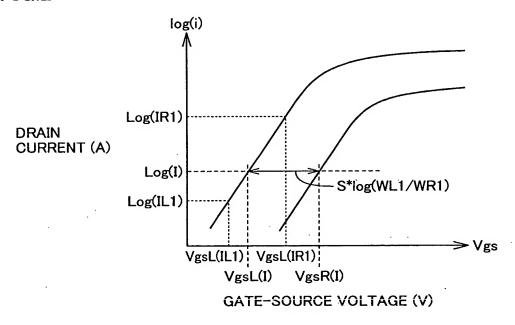


FIG.3

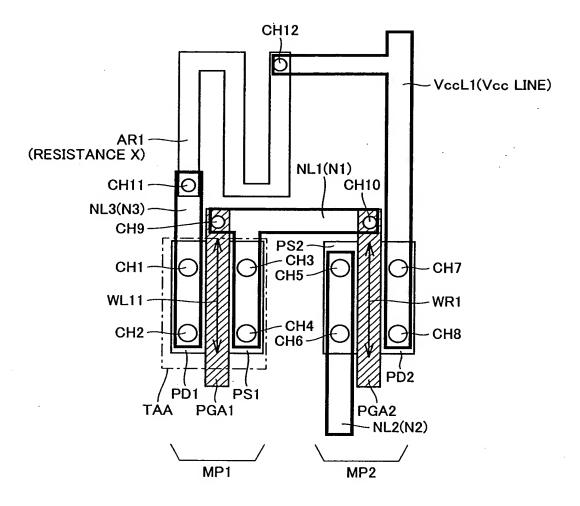
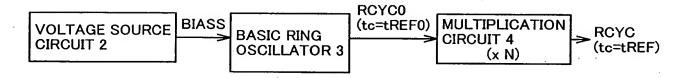


FIG.4



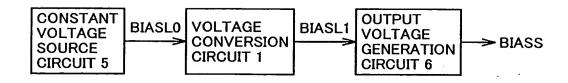
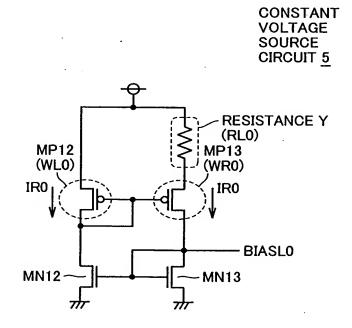
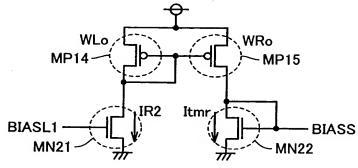


FIG.6







Itmr=(WRo/WLo) x IR2 Ic  $\propto$  Itmr

FIG.8

# BASIC RING OSCILLATOR $\underline{\mathbf{3}}$

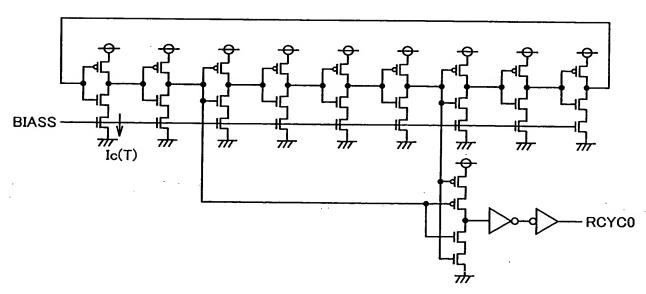
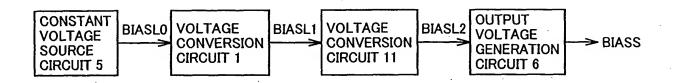
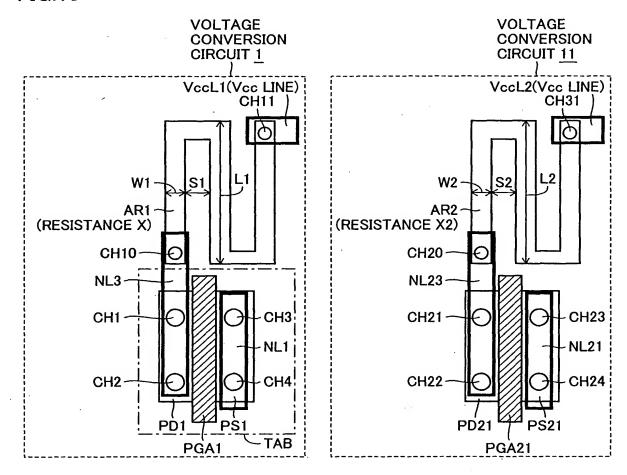


FIG.9

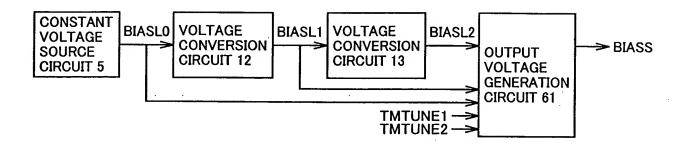


**FIG.10** 

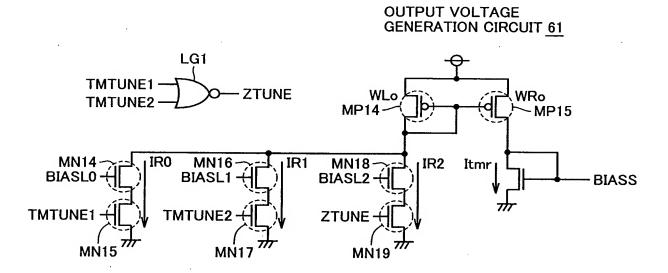


# **FIG.11**

### **VOLTAGE SOURCE CIRCUIT 22**

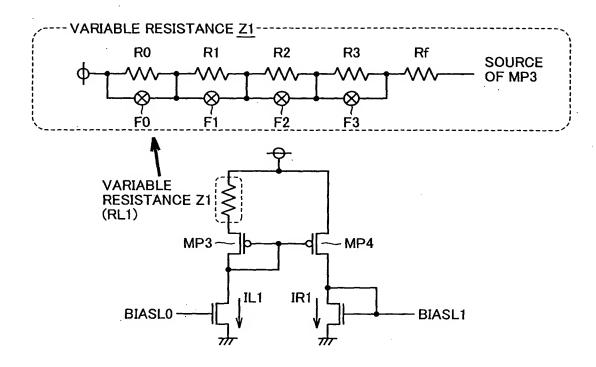


# **FIG.12**

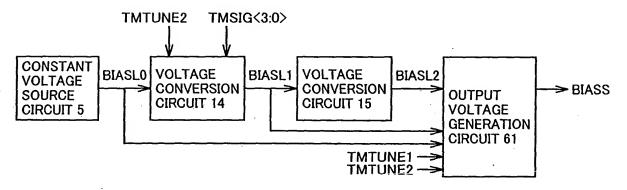


**FIG.13** 

VOLTAGE CONVERSION CIRCUIT 12

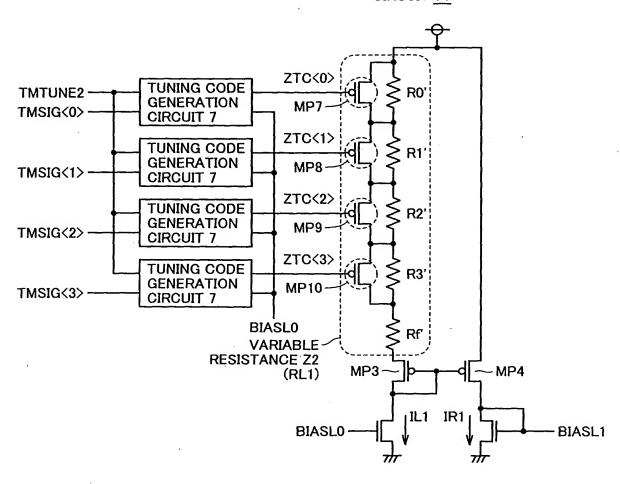


**FIG.14** 



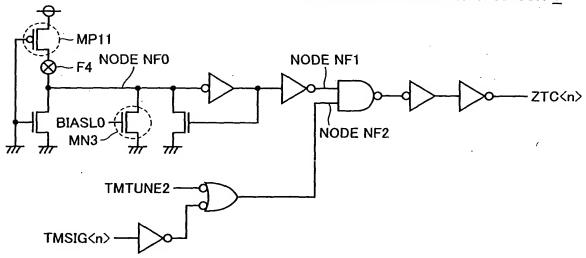
**FIG.15** 

# VOLTAGE CONVERSION CIRCUIT 14

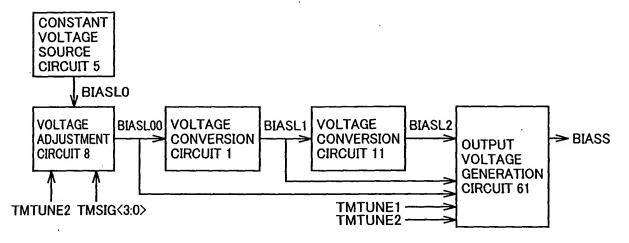


**FIG.16** 

#### **TUNING CODE GENERATION CIRCUIT 7**



**FIG.17** 



**FIG.18** 

### VOLTAGE ADJUSTMENT CIRCUIT 8

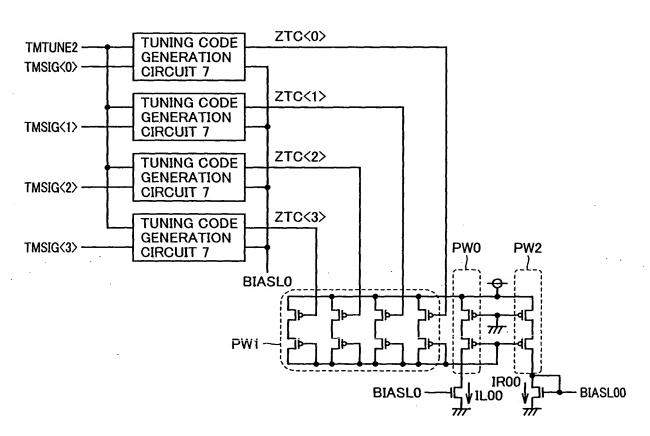


FIG.19

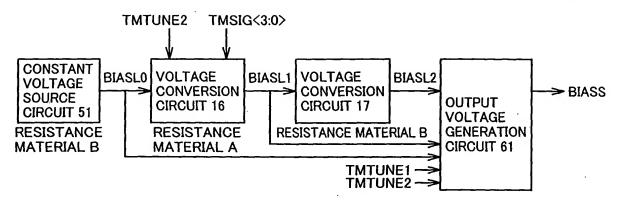
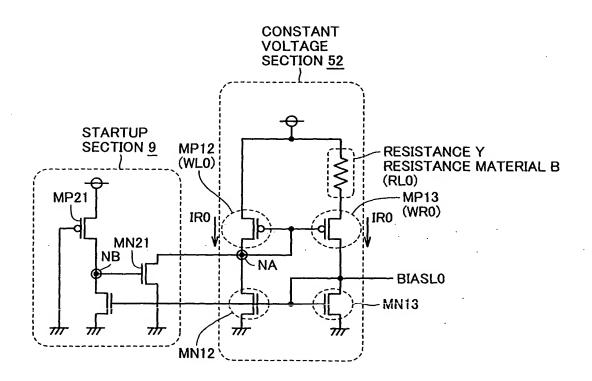


FIG.20

# CONSTANT VOLTAGE SOURCE CIRCUIT 51



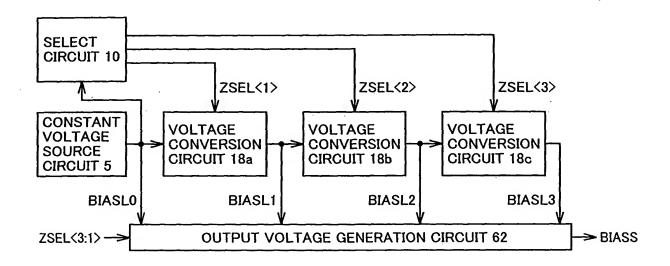
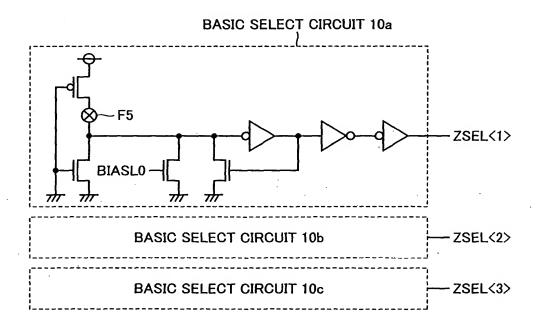


FIG.22

## SELECT CIRCUIT 10



VOLTAGE CONVERSION CIRCUIT 18a

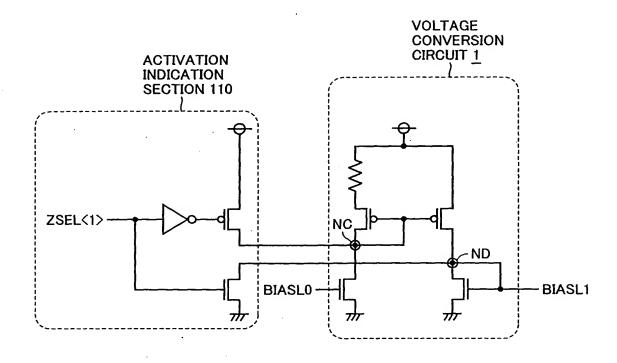
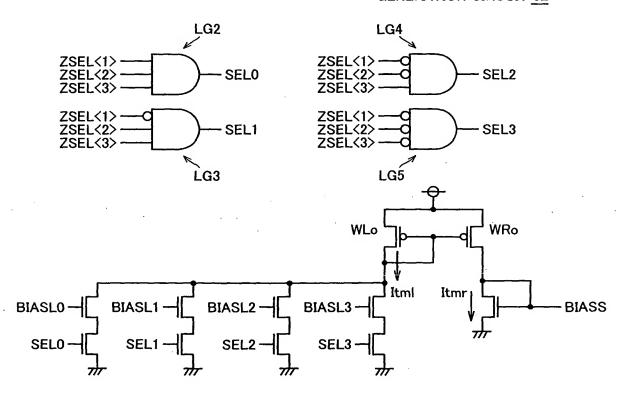


FIG.24

#### OUTPUT VOLTAGE GENERATION CIRCUIT 62



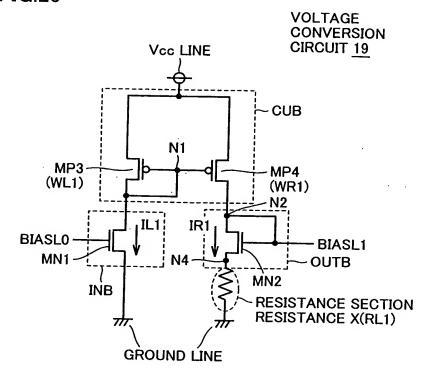


FIG.26

